

WaveRunner® 6000 Series

6030 6050/6051 6100 6200



- 350 MHz, 500 MHz, 1 GHz and 2 GHz Bandwidths
- 5 GS/s on All Channels (10 GS/s on 2 Ch for 6100 and 6200)
- 1 Mpts on All Channels, Expandable to 12/24 Mpts
- Compact and Lightweight
- Easy User Interface
- New 2.5 mm Passive Probe
- Touch Screen Interface
- Vertical Controls for Each Channel
- USB 2.0 and 802.3xx LAN Ports
- Open Windows 2000



Excellent Performance, Great Price, Easy to Use

LeCroy's WaveRunner 6000 Series is built to be the world's best everyday bench oscilloscope. It offers the best acquisition specifications, a user interface that makes it easy to perform the most common oscilloscope functions, industry-leading long term support and a "feel" that makes the oscilloscope a pleasure to drive.

For the first time, LeCroy has combined the type of high performance front amplifier, ADC, memory and triggering used in more expensive oscilloscopes and designed it all into a very affordable package. The WaveRunner 6000 Series also introduces a user interface that makes viewing and measuring signals simple and fast.

With the WaveRunner 6000 Series, all viewing controls and basic oscilloscope functions are easily at hand using front panel knobs. You get fast views and can zoom in to see details on the bright touch panel color screen. Or use the simple and intuitive controls to call up exactly the measurements you need.

The WaveRunner 6000 Series includes an industry-leading signal acquisition path, which provides a 5 GS/s ADC on every

channel (Model 6050 and above) and 1 Mbyte of standard memory. No need to worry about the undersampling or aliasing caused by slower ADCs or shorter memories on other oscilloscopes.

The WaveRunner 6000 Series comes standard with the new PP007 500 MHz passive probe (one per channel). This 2.5 mm high impedance probe offers excellent characteristics for probing everyday signals. LeCroy also offers a wide range of optional single-ended and differential active probes, current probes, optical to electrical (O/E) converters and differential amplifiers.

Lastly, we decided to architect the oscilloscope so that users could add just the functionality they want. There are options for testing power devices, serial data mask testing, jitter and timing analysis, and for a wide variety of probes, O/E converters and other application specific devices.

Altogether, the WaveRunner 6000 Series sets a new industry standard for high performance at low price in everyday bench oscilloscopes.





Specifications

Vontical System	WaveRunner	WaveRunner	WaveRunner	WaveRunner	WaveRunner		
Vertical System	6030	6050	6051	6100	6200		
Nominal Analog Bandwidth @ 50 Ω (-3 dB)	350 MHz	500 MHz	500 MHz	1 GHz	2 GHz		
Rise Time (Typical)	1 ns	750 ps	750 ps	200 ps	225 ps		
Input Channels	4	4	2	4	4		
Bandwidth Limiters	25 MHz; 200 MHz						
Input Impedance	1MΩ//<20pF (10 MΩ // 9.5pF using PP007 probe)						
Input Coupling	50 Ω : DC, 1M Ω : AC, DC, GND						
Maximum Input Voltage, 50 Ohm	50 Ω :5 Vrms, 1 M Ω :250 V max (Peak AC:≤ 10 kHz + DC)						
Channel to Channel Isolation	>40dB @ <100MHz (>30dB @ full bandwidth)						
Vertical Resolution		8 bits; u	p to 11 with enhanced resolution	(ERES)			
Sensitivity		50 Ω : 2 mV/div – 1 V	/div fully variable; 1 M Ω : 2 mV – 10	0 V/div fully variable			
DC Gain Accuracy		±1.0% of fu	ıll scale (typical); ±1.5% full scale (v	varranted)			
Offset Range			50 Ω: ± 400 mV @ 2-4.99 mV/div				
			± 1.0 V @ 5-99 mV/div				
	± 10 V @ 100 V/div - 1V/div						
			1 MΩ: ± 500 mV @ 2-4.99 mV/div				
			± 1.0 V @ 5-99 mV/div ± 10 V @ 100 mV/div - 1V/div				
			± 100 V @ 1 - 10V/div				
Offset Accuracy		+1'	1.5% + 0.5% of offset value + 1 m\	/)			
Probing System			BNC or Probus	• /			
			BINC OF FIODUS				
Timebase System							
Timebases	Inte	rnal timebase common to all in	put channels; an external clock m	ay be applied at the auxiliary in	out		
Time/Division Range			20 ps/div – 10 s/div				
Math & Zoom Traces			ent zoom and 4 math/zoom trace				
			es available with XMATH (Advance				
Clock Accuracy		± 5	ppm @ 25° C (± 10ppm @ 5-40°	C)			
Jitter Noise Floor		2 ps rm	ns (typical, 5ps warranted) @ 100 n	nV/div			
Time Interval Accuracy		C	lock Accuracy + Jitter Noise Floor				
Sample Rate & Delay Time Accuracy			Equal to Clock Accuracy				
Trigger & Interpolator Jitter (RMS)			≤ 3 ps rms (typical)				
Channel to Channel Deskew Range			±4.5 ns				
External Sample Clock			DC to 1 GHz; 50 Ω BNC input				
Roll Mode		Switches Automa	tically at t/div > .5 S/div or sample	rate < 20 ks/sec			
Acquisition System							
Single-Shot Sample Rate/Ch	2.5 GS/s		5 G				
Interleaved Sample Rate (2 Ch)		N/A	200.657	100	GS/s		
Random Interleaved Sampling (RIS)			200 GS/s				
Trigger Rate		10.000	125,000 waveforms/second	le en ele escente			
Sequence Mode Acquisition		TO,000 segitiei	nts max. Trigger time recorded wit	ireacii everit.			
Sequence Time Stamp Resolution			1 ns				
Minimum time Between Sequential Segments	114 (451) (214 (251)	8 µs	114 (461)	(214/261)		
Acquisition Memory – Standard	1M (4Ch) / 2	ZM (ZCn)	1M (2Ch) / 2M (1Ch)	IM (4Cn)	/ 2M (2Ch)		
Acquisition Memory – Option S			2M / 4M				
Acquisition Memory – Option M			4M / 8M				
Acquisition Memory – Option L			8M/16M				
Acquisition Memory – Option VL			12M/24M				
Acquisition Processing							
Time Resolution (min, Single-shot)							
Averaging			200 ps (5 GS/s) [100 ps (10 GS/s)]				
			200 ps (5 GS/s) [100 ps (10 GS/s)] nd continuous averaging to 1 milli	on sweeps			
ERES		Summed ar	nd continuous averaging to 1 milli				
ERES		Summed ar Fro	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolutio	n			
ERES Envelope (Extrema)		Summed ar Fro	nd continuous averaging to 1 milliom 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million	n			
ERES Envelope (Extrema) Interpolation		Summed ar Fro	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolutio	n			
ERES Envelope (Extrema) Interpolation Trigger System		Summed ar Fro	nd continuous averaging to 1 milliom 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x	n			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes		Summed ar Frr Envelope	nd continuous averaging to 1 milliom 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop	n sweeps			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources		Summed ar Frr Envelope	nd continuous averaging to 1 milliom 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level	n sweeps			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling		Summed ar Frr Envelope Any input channel, Exter	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ	n sweeps unique to each source			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources		Summed ar Fre Envelope Any input channel, Exter 0–100% of men	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 \(\Omega\$, GND, DC1M\(\Omega\$, AC1M\(\Omega\$ nory size (adjustable in 1% increm	n sweeps unique to each source ents, or 100 ns)			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling		Summed ar Fre Envelope Any input channel, Exter 0–100% of men	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ	n sweeps unique to each source ents, or 100 ns)			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 \(\Omega\$, GND, DC1M\(\Omega\$, AC1M\(\Omega\$ nory size (adjustable in 1% increm	n sweeps unique to each source ents, or 100 ns) 0 seconds			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω , GND, DC1M Ω , AC1M Ω nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400	n sweeps unique to each source ents, or 100 ns) 0 seconds			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 r	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ are size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center	n sweeps Lunique to each source ents, or 100 ns) 0 seconds 1st			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 r	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >750 MHz with Edge Trigger; 1 dirigger @≥10 mV (subject to bance)	n sweeps Lunique to each source ents, or 100 ns) 0 seconds 1st			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 r	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ are size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center	n sweeps Lunique to each source ents, or 100 ns) 0 seconds 1st			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 r	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >750 MHz with Edge Trigger; 1 dirigger @≥10 mV (subject to bance)	n sweeps Lunique to each source ents, or 100 ns) 0 seconds 1st			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 r	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop and, Ext/10, or Line; slope and level DC50 Ω , GND, DC1M Ω , AC1M Ω are size (adjustable in 1% incremated to 10,000 divisions or 86,400 as or 20 s or 1 to 99,999,999 event \pm 5 div from center >750 MHz with Edge Trigger; 1 divirigger @ 210 mV (subject to bance \pm 3% full scale \pm 2mV (typical)	n sweeps Lunique to each source ents, or 100 ns) 0 seconds 1st			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle 2 r 2 divisions at 750 MHz max with SMART T	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >>750 MHz with Edge Trigger; 1 divirigger @≥10 mV (subject to bance ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV	n sweeps unique to each source ents, or 100 ns) 0 seconds is v at 750 MHz width limit of oscilloscope)			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle 2 r 2 divisions at 750 MHz max with SMART T	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop and, Ext/10, or Line; slope and level DC50 Ω , GND, DC1M Ω , AC1M Ω are size (adjustable in 1% incremated to 10,000 divisions or 86,400 as or 20 s or 1 to 99,999,999 event \pm 5 div from center >750 MHz with Edge Trigger; 1 divirigger @ 210 mV (subject to bance \pm 3% full scale \pm 2mV (typical)	n sweeps unique to each source ents, or 100 ns) 0 seconds is v at 750 MHz width limit of oscilloscope)			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line SMART Triggers®		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle 2 r 2 divisions at 750 MHz max with SMART T	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >>750 MHz with Edge Trigger; 1 divirigger @≥10 mV (subject to bance ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV	n sweeps unique to each source ents, or 100 ns) 0 seconds is v at 750 MHz width limit of oscilloscope)			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 t 2 divisions at 750 MHz max with SMART T Triggers when signal	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center 5750 MHz with Edge Trigger; 1 divirigger @ ≥10 mV (subject to banc ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV meets slope (positive or negative) any if a defined state or edge occu	n sweeps I unique to each source ents, or 100 ns) D seconds ss v at 750 MHz width limit of oscilloscope) I and level condition rred on another input source.			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line SMART Triggers® State or Edge Qualified		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle 2 t 2 divisions at 750 MHz max with SMART T Triggers when signal Triggers on any input source or Delay betw.	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >750 MHz with Edge Trigger; 1 divirgger @ ≥10 mV (subject to bance ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV meets slope (positive or negative) any if a defined state or edge occureen sources is selectable by time	n sweeps unique to each source ents, or 100 ns)) seconds ss v at 750 MHz lwidth limit of oscilloscope) u and level condition rred on another input source . or events.			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line SMART Triggers® State or Edge Qualified		Summed ar Fre Envelope Any input channel, Exter 0–100% of mem The smalle 2 r 2 divisions at 750 MHz max with SMART T Triggers when signal Triggers on any input source or Delay betw. Triggers if signal drops o	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ are size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >750 MHz with Edge Trigger; 1 divirigger @ ≥10 mV (subject to bance ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV meets slope (positive or negative) nly if a defined state or edge occurren sources is selectable by time ut for longer than selected time but for longe	n sweeps I unique to each source ents, or 100 ns) 0 seconds is v at 750 MHz width limit of oscilloscope) I and level condition rred on another input source or events. Setween 25 ns and 20 s.			
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line SMART Triggers® State or Edge Qualified		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle 2 r 2 divisions at: 750 MHz max with SMART T Triggers when signal Triggers on any input source or Delay betw Triggers if signal drops o (AND, NAND, OR, NOR) of 5 inp	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ nory size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >> 750 MHz with Edge Trigger; 1 divirgger @≥10 mV (subject to bance ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV meets slope (positive or negative) where is selectable by time further or the survey of the selectable by time for longer than selected time buts (4 channels and external trigg	n sweeps Junique to each source ents, or 100 ns) Disconds St Vat 750 MHz width limit of oscilloscope) Jand level condition rred on another input source or events. setween 25 ns and 20 s. er input). Each source can be h	igh, low, or don't care.		
ERES Envelope (Extrema) Interpolation Trigger System Trigger Modes Sources Trigger Coupling Pre-trigger delay Post-trigger delay Hold-off Internal trigger level range Max trigger frequency Trigger Level DC Accuracy External trigger range Basic Triggers Edge/Slope/Line SMART Triggers® State or Edge Qualified Dropout		Summed ar Fro Envelope Any input channel, Exter 0–100% of mem The smalle 2 r 2 divisions at: 750 MHz max with SMART T Triggers when signal Triggers on any input source or Delay betw Triggers if signal drops o (AND, NAND, OR, NOR) of 5 inp	nd continuous averaging to 1 million 8.5 to 11 bits vertical resolution, floor, and roof for up to 1 million Linear, Sinx/x Normal, Auto, Single, Stop nal, Ext/10, or Line; slope and level DC50 Ω, GND, DC1MΩ, AC1MΩ are size (adjustable in 1% increment of 0 to 10,000 divisions or 86,400 ns or 20 s or 1 to 99,999,999 event ±5 div from center >750 MHz with Edge Trigger; 1 divirigger @ ≥10 mV (subject to bance ±3% full scale ±2mV (typical) EXT/10 ±4V; EXT ±400mV meets slope (positive or negative) nly if a defined state or edge occurren sources is selectable by time ut for longer than selected time but for longe	n sweeps Junique to each source ents, or 100 ns) Disconds St Vat 750 MHz width limit of oscilloscope) Jand level condition rred on another input source or events. setween 25 ns and 20 s. er input). Each source can be h	igh, low, or don't care.		



Specifications

with Exclusion Technology	WaveRunner 6030	WaveRunner 6050	WaveRunner 6051	WaveRunner 6100	WaveRunner 6200	
litch & Pulse Width				500 ps to 20 s or on intermittent		
gnal or Pattern Width	Т			ps to 20 s or on intermittent faul	ts.	
gnal or Pattern Interval			n intervals selectable between 2			
meout (State/Edge Qualified)	Triggers on any source if a given state (or transition edge) has occurred on another source. Delay between sources is 10ns to 20 s, or 1 to 99,999,999 events. Trigger on internittent faults by specifying the normal width or period.					
cclusion Triggering		Irigger on intemit	tent faults by specifying the norr	nal width or period.		
utomatic Setup						
rtical Find Scale	Automatically sets timebase, trigger, and sensitivity to display a wide range Automatically sets the vertical sensitivity and offset for the selected channels to display					
robes						
obes			el standard; Optional passive and	'		
obe System; Probus ale Factors	Automatically detects and supports a variety of compatible probes Automatically or manually selected, depending on probe used					
Color Waveform Display		C 0488 :	ITTICO NI IN IN			
/pe esolution		Color 8.4" flat-p	panel TFT-LCD with high resolution SVGA; 800 x 600 pixels	on touch screen		
eal Time Clock	Dates hours minut	es seconds displayed with wave	<u> </u>	support to synchronize to precis	sion internet clocks	
umber of Traces	Bates, noars, minat			zoom, memory, and math traces.		
rid Styles			e, Dual, Quad, Octal, XY, Single + >			
/aveform Styles			Sample dots joined or dots only	/		
Inalog Persistence Display					-	
nalog & Color-Graded Persistence		Variable saturation	levels; stores each trace's persiste	nce data in memory.		
ersistence Selections			ect analog, color, or three-dimens			
race Selection			rsistence on all or any combinat			
ersistence			ng Time Select from 500 ms to in			
weeps Displayed		All accumulate	ed, or all accumulated with last tr	ace highlighted.		
Coom Expansion Traces	Display up	to 4 Zoom and 4 Math/Zoom to	races: 8 Math/Zoom traces availa	ble with XMAP (Master Analysis p	package) or	
			XMATH (Advanced Math package			
PU						
rocessor			z or better with MS Windows 20			
rocessing Memory		256 MB on Sto	d & M option; 512 MB with L opt	ion & VL option		
nternal Waveform Memory			m Memory (store full-length wa umber of files limited only by dat	veform with 16 bits/data point) o	r	
Setup Storage		,				
ront Panel and Instrument Status		Store to the internal hard dri	ve, over the network, or to a USB	-connected peripheral device.		
nterface						
emote Control		Via Windows A	automation, or via LeCroy Remote	e Command Set		
PIB Port (Optional)			Supports IEEE – 488.2			
.l D .		10/1000				
			ase-T Ethernet interface (RJ-45 c			
ISB Ports	Standa	5 USB ports (one on fro	ont of instrument) support Wind	ows compatible devices	/ mode	
ISB Ports xtenal Monitor Port	Standa	5 USB ports (one on fro	ont of instrument) support Wind ible DB-15; connect a second mo		/ mode.	
ISB Ports xtenal Monitor Port arallel Port	Standa	5 USB ports (one on fro rd 15-pin D-Type SVGA-compati	ont of instrument) support Wind	ows compatible devices onitor to use dual-monitor display	/ mode.	
ISB Ports xtenal Monitor Port arallel Port erial Port	Standa	5 USB ports (one on fro rd 15-pin D-Type SVGA-compati	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard	ows compatible devices onitor to use dual-monitor display	/ mode.	
ISB Ports xtenal Monitor Port arallel Port erial Port Auxiliary Input	Standa	5 USB ports (one on fro rd 15-pin D-Type SVGA-compati DB-9 COM	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard	ows compatible devices unitor to use dual-monitor display upe control)	v mode.	
ithernet Port JSB Ports Extend Monitor Port Farallel Port Serial Port Auxiliary Input Signal Types General	Standa	5 USB ports (one on fro rd 15-pin D-Type SVGA-compati DB-9 CON Selected from Ext	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard 11 port (not for remote oscillosco ternal Trigger or External Clock in	ows compatible devices unitor to use dual-monitor display upe control) put on front panel	/ mode.	
ISB Ports xtenal Monitor Port arallel Port erial Port Auxiliary Input ignal Types General uto Calibration		5 USB ports (one on fro rd 15-pin D-Type SVGA-compati DB-9 COM Selected from Ext Ensures specified DC	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard 11 port (not for remote oscillosco ternal Trigger or External Clock in and timing accuracy is maintain	ows compatible devices initor to use dual-monitor display inpector per control) put on front panel ed for 1 year minimum		
ISB Ports xtenal Monitor Port arallel Port erial Port Auxiliary Input ignal Types General uto Calibration		5 USB ports (one on fro rd 15-pin D-Type SVGA-compati DB-9 COM Selected from Ext Ensures specified DC D-120 Vrms at 50/60/400 Hz; 200	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard 11 port (not for remote oscillosco ternal Trigger or External Clock in and timing accuracy is maintain	ows compatible devices unitor to use dual-monitor display upe control) put on front panel ed for 1 year minimum upe and frequency tolerance: ±10		
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ISB Ports xtenal Monitor Port arallel Port erial Port Auxiliary Input ignal Types General uto Calibration ower invironmental emperature: Operating emperature: Operating lumidity: Operating	10	5 USB ports (one on first of 15-pin D-Type SVGA-compating 15-pin D-Type SV	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard 11 port (not for remote oscillosco ternal Trigger or External Clock in and timing accuracy is maintain -240 Vrms at 50/60 Hz (Max volt. y: 300V CAT II; Max. Power Consu +5°C to 40°C -20°C to +60°C p to ±30°C, Upper limit derates t	ows compatible devices initor to use dual-monitor display ope control) put on front panel ed for 1 year minimum age and frequency tolerance: ±10 mption: 400VA/400W)96);	
ISB Ports xtenal Monitor Port arallel Port erial Port Auxiliary Input ignal Types ieneral uto Calibration ower invironmental emperature: Operating lumidity: Operating lumidity: Non-Operating	10	5 USB ports (one on fro rd 15-pin D-Type SVGA-compati DB-9 CON Selected from Ext Ensures specified DC D-120 Vrms at 50/60/400 Hz; 200 Installation Categor	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard 11 port (not for remote oscillosco ternal Trigger or External Clock in and timing accuracy is maintain -240 Vrms at 50/60 Hz (Max volt: y: 300V CAT II; Max. Power Consu +5°C to 40°C -20°C to +60°C p to ±30°C, Upper limit derates t (non-condensing) as tested per	ows compatible devices initor to use dual-monitor display spe control) put on front panel ed for 1 year minimum age and frequency tolerance: ±10 mption: 400VA/400W)96);	
ISB Ports Extenal Monitor Port Fort Fort Auxiliary Input Fort Fort	10	5 USB ports (one on fro rd 15-pin D-Type SVGA-compati DB-9 CON Selected from Ext Ensures specified DC D-120 Vrms at 50/60/400 Hz; 200 Installation Categor	ont of instrument) support Wind ible DB-15; connect a second month of instrument is standard in port (not for remote oscilloscotternal Trigger or External Clock in and timing accuracy is maintain -240 Vrms at 50/60 Hz (Max voltzy: 300V CAT II; Max. Power Consumer of the	ows compatible devices initor to use dual-monitor display spe control) put on front panel ed for 1 year minimum age and frequency tolerance: ±10 mption: 400VA/400W)96);	
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SB Ports ktenal Monitor Port arallel Port erial Port auxiliary Input gnal Types ieneral uto Calibration ower anyironmental emperature: Operating emidity: Operating umidity: Non-Operating littude: Operating littude: Non-Operating	10	5 USB ports (one on fird 15-pin D-Type SVGA-compating 15-pin D-Type SVGA-c	ont of instrument) support Wind ible DB-15; connect a second month of island ible DB-15; connect a second ible DB	ows compatible devices initor to use dual-monitor display spe control) put on front panel ed for 1 year minimum age and frequency tolerance: ±10 mption: 400VA/400W o 50% RH (non-condensing) at 44 MIL-PRF-28800F Im (2,000 ft) at 40°C)96);	
SB Ports ktenal Monitor Port arallel Port erial Port Auxiliary Input ignal Types ieneral uto Calibration ower invironmental emperature: Operating umidity: Operating umidity: Non-Operating lititude: Operating lititude: Non-Operating lititude: Operating	10	5 USB ports (one on fird 15-pin D-Type SVGA-compating 15-pin D-Type SVGA-c	ont of instrument) support Wind ible DB-15; connect a second mo 1 standard 11 port (not for remote oscilloscoternal Trigger or External Clock in and timing accuracy is maintain -240 Vrms at 50/60 Hz (Max voltz y: 300V CAT II; Max. Power Consu +5°C to 40°C -20°C to +60°C pt to ±30°C, Upper limit derates to (non-condensing) as tested per max at up to 25°C, derates to 610 12,190m (40,000 ft) grms, 5 Hz, 15 minutes in each of	ows compatible devices onitor to use dual-monitor display ope control) put on front panel ed for 1 year minimum age and frequency tolerance: ±10 mption: 400VA/400W o 50% RH (non-condensing) at 44 MIL-PRF-28800F Im (2,000 ft) at 40°C of three orthogonal axes)96);	
ISB Ports Ixtenal Monitor Port Ixtenal Mon	10	5 USB ports (one on fird 15-pin D-Type SVGA-compating 15-pin D-Type SVGA-c	ont of instrument) support Wind ible DB-15; connect a second month of the DB-15; conn	ows compatible devices initor to use dual-monitor display ope control) put on front panel ed for 1 year minimum age and frequency tolerance: ±10 mption: 400VA/400W o 50% RH (non-condensing) at 44 MIL-PRF-28800F Om (2,000 ft) at 40°C of three orthogonal axes ach of three orthogonal axes	0%); 0°C	
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